Xilinx Legal

PART B - FEE(S) TRANSMITTAL

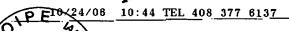
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10/	SAN JOSE, CA 25/2006 RMEBRAH1 00		0737280	Pat Tompkins		ins	(Depasitor's name)	
						2 Rins	(Signowre)	
01	FC:1501 1400.	00 DA -			October 2	2006	(Date)	
	APPLICATION NO.	· FILING DATE		FIRST.NAMED INVESTOR	ATTO	RNEY DOCKET NO.	CONFIRMATION NO.	
	10/737,280	12/16/2003	<u>.</u>	Stephen M. Trimberger		X-1188 US	6162	
	TITLE OF INVENTION ARRAYS IN PROGRAM			IDING PROGRAMMABL	E WIDTH AND ERROR	CORRECTION IN N	MEMORY	
	APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE	
	nonprovisional	NO	\$1400	\$0	\$0	\$1400	01/04/2007	
	EXAMINER		ART UNIT	CLASS-SUBCLASS				
	ABRAHAM, ESAW T		2133	714-763000				
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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment. (A) NAME OF ASSIGNEE (B) RESIDENCE: (CITY and STATE OR COUNTRY) XILINX, INC. San Jose, California 95124							document has been filed for	
	Please check the appropr	iate assignee eategory o	r categories (will not be p	rinled on the putent) :	Individual 🗵 Corporat	ion or other private gr	oup entity Government	
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Inventor(s):

Stephen M. Trimberger

Assignee: XILINX, INC. Serial No.: 10/737,280

conf.no. 6162

Filed:

2313-1450

December 16, 2003

Titte: Methods and Structures for Providing Programmable Width and Error Correction in

Memory Arrays in Programmable Logic Devices

Docket No.: X-1188 US

Enclosed: PReturn Receipt Postcard

→ Fee Transmittal - Part B

Date: October 24, 2006

VIA FACSIMILE 571-273-2885

Atty/Sec: KAC/pat